

YO999-153DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re divisional patent application of 09/325,732 filed on June 4, 1999

Chen, et al.

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: METHOD FOR FABRICATING COMPLEMENTARY METAL OXIDE
SEMICONDUCTOR (CMOS) DEVICES ON A MIXED BULK AND SILICON-ON-
INSULATOR (SOI) SUBSTRATE

Assistant Commissioner of Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to examination on the merits and calculation of the filing fee, please amend the
above-identified application as follows:

IN THE SPECIFICATION:

Page 1, before line 5, insert --The present Application is a Divisional Application of
U.S. Patent Application No. 09/325,732, filed on June 4, 1999.--.

Page 6, line 11, delete "Figure 6 illustrate" and insert --Figures 6A-6B--.

Page 7, line 4, delete "1A-6" and insert --1A-6B--.

Page 14, line 17, delete "Figure 6" and insert --Figures 6A and 6B--.

IN THE CLAIMS:

Please cancel claims 1-23 without prejudice or disclaimer.

REMARKS

Claims 1-23 have been canceled to allow prosecution of claims 24-28.

HL
ReAnett A
12/16/99
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